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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Yoshitaka UEDA et al.**

Group Art Unit: **2816**

Serial Number: **10/760,475**

Examiner: **An T. Lun**

Filed: **January 21, 2004**

Confirmation No.: **9800**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT RELATED TO A
CIRCUIT OPERATING ON THE BASIS OF A CLOCK SIGNAL**

Attorney Docket Number: **001681A**

Customer Number: **38834**

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop: Missing Parts

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

January 27, 2006

Sir:



Please supply the undersigned attorney with a corrected filing receipt for the above-identified application. The undersigned also respectfully requests that the Patent and Trademark Office records be amended to reflect the correction.

In reviewing the Official Filing Receipt, we noted an error in Domestic Priority Data. The Domestic Priority Data should read -- This Application is a Division of 09/745,990, filed December 26, 2000--. It is noted that the application was incorrectly filed as a continuation application instead of a divisional application. Applicants also submit herewith a Supplemental Application Data Sheet, reflecting the correct Domestic Priority Data.

Request for Corrected Filing Receipt
10/760,475
001681A

If any fees are required in connection with this paper, please charge Deposit Account No.
50-2866.

Respectfully submitted,
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Supplemental Application Data Sheet



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Supplemental Application Data Sheet



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Application Information

Title Line One: SEMICONDUCTOR INTEGRATED CIRCUIT RELATED TO A
Title Line Two: CIRCUIT OPERATING ON THE BASIS OF A CLOCK SIGNAL

Total Drawing Sheets:
Docket Number: 001681A
Application Type:

Representative Information

Customer No.: 38834

Domestic Priority Information

This application is a: Division of
Application One: 09/745,990
Filing Date: December 26, 2000

Foreign Priority Information

Foreign Application One: JP 11-372781
Filing Date: December 28, 1999
Country: Japan
Priority Claimed: Yes

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